



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Koji Hayashi
Serial No. : 09/717,771
Filed : November 21, 2000
Title : CONTROLLER FOR DATA RECORDER

Art Unit : 2651
Examiner : A. Psitos

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m05
12-16-02
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DEC 13 2002

Technology Center 2600

BOX AF
Commissioner for Patents
Washington, D.C. 20231

RESPONSE

In response to the final action mailed June 6, 2002, please amend the application as follows:

In the claims:

Please cancel claim 6 without prejudice.

Please amend claims 1-4 and 7-9 as follows:

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1. (Twice Amended) A control circuit for a data recorder, wherein the data recorder records data on a recording data by emitting a laser beam against a recording medium and has a buffer memory for temporarily storing data, an encoder for encoding the data of the buffer memory into the recording data, and a laser drive circuit for controlling the power level of the laser beam in accordance with the level of the recording data, wherein the data recorder generates the laser beam at a relatively high power level sufficient to form a recording pit on a recording layer of the recording medium when the level of the recording data is high, and the data recorder generates the laser beam at a relatively low power level insufficient to form a recording pit on the recording layer of the recording medium when the level of the recording data is low, the control circuit comprising:

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

December 6, 2002

Date of Deposit

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Matthew G. Doherty

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